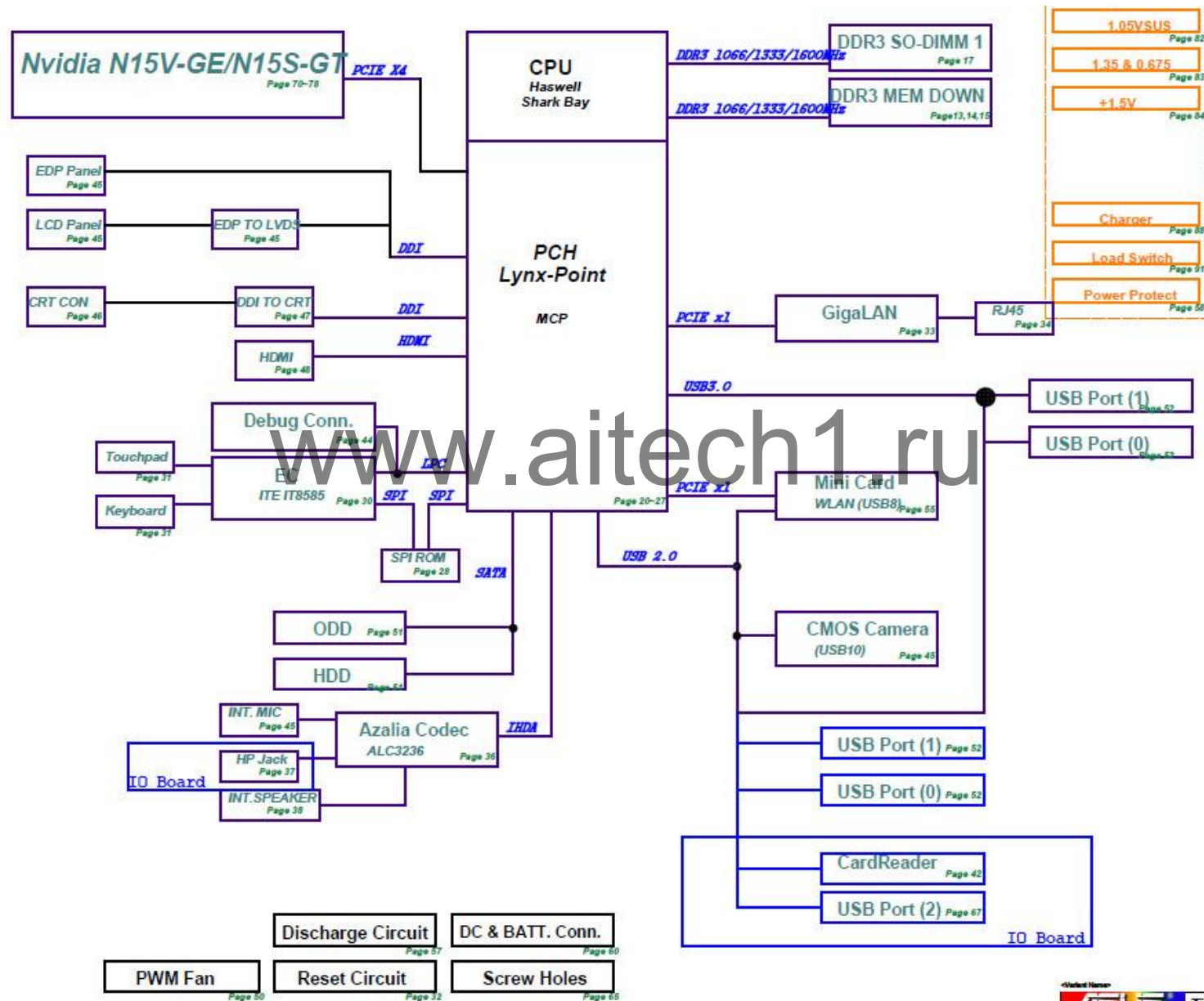
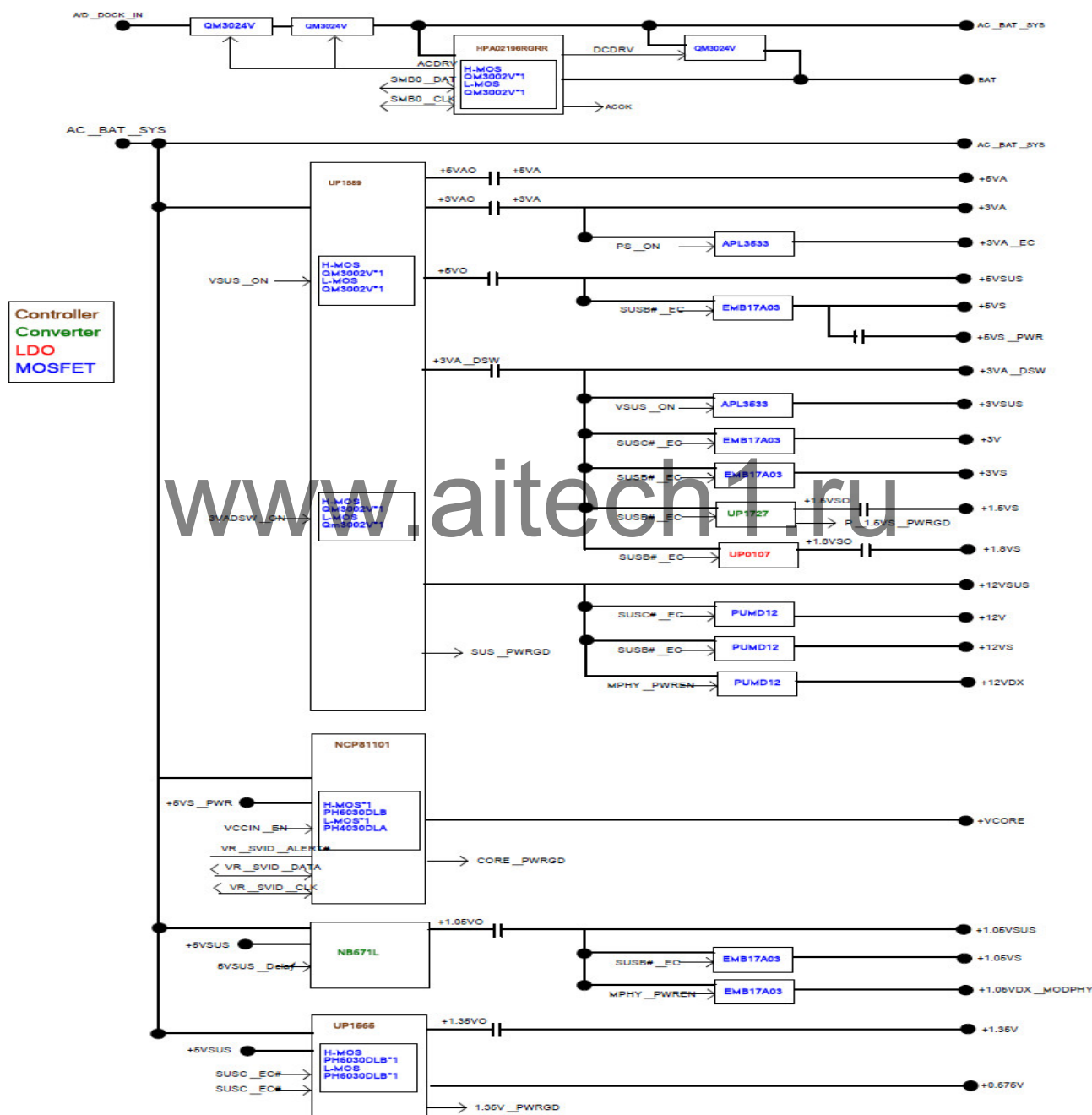


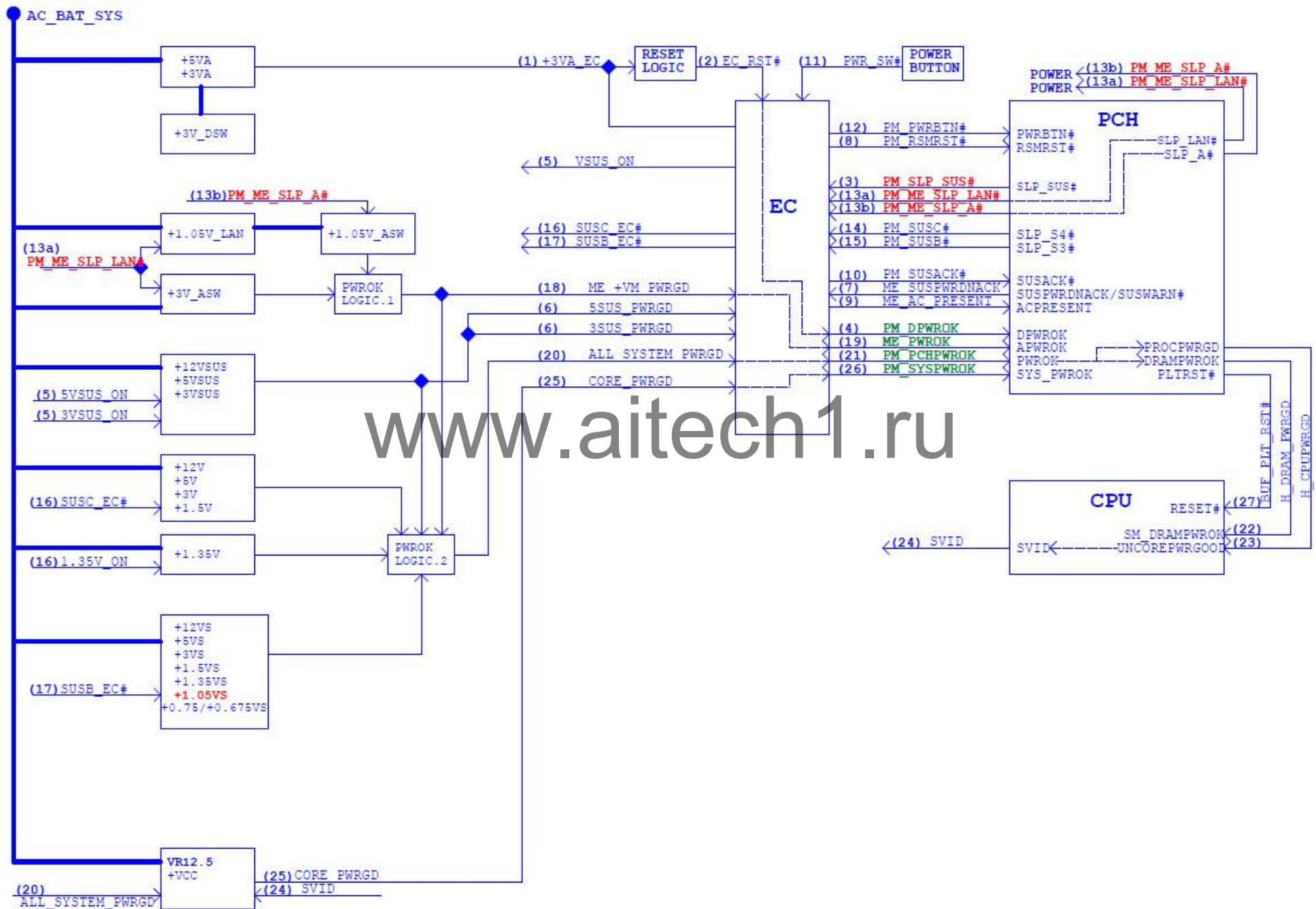
BLOCK DIAGRAM



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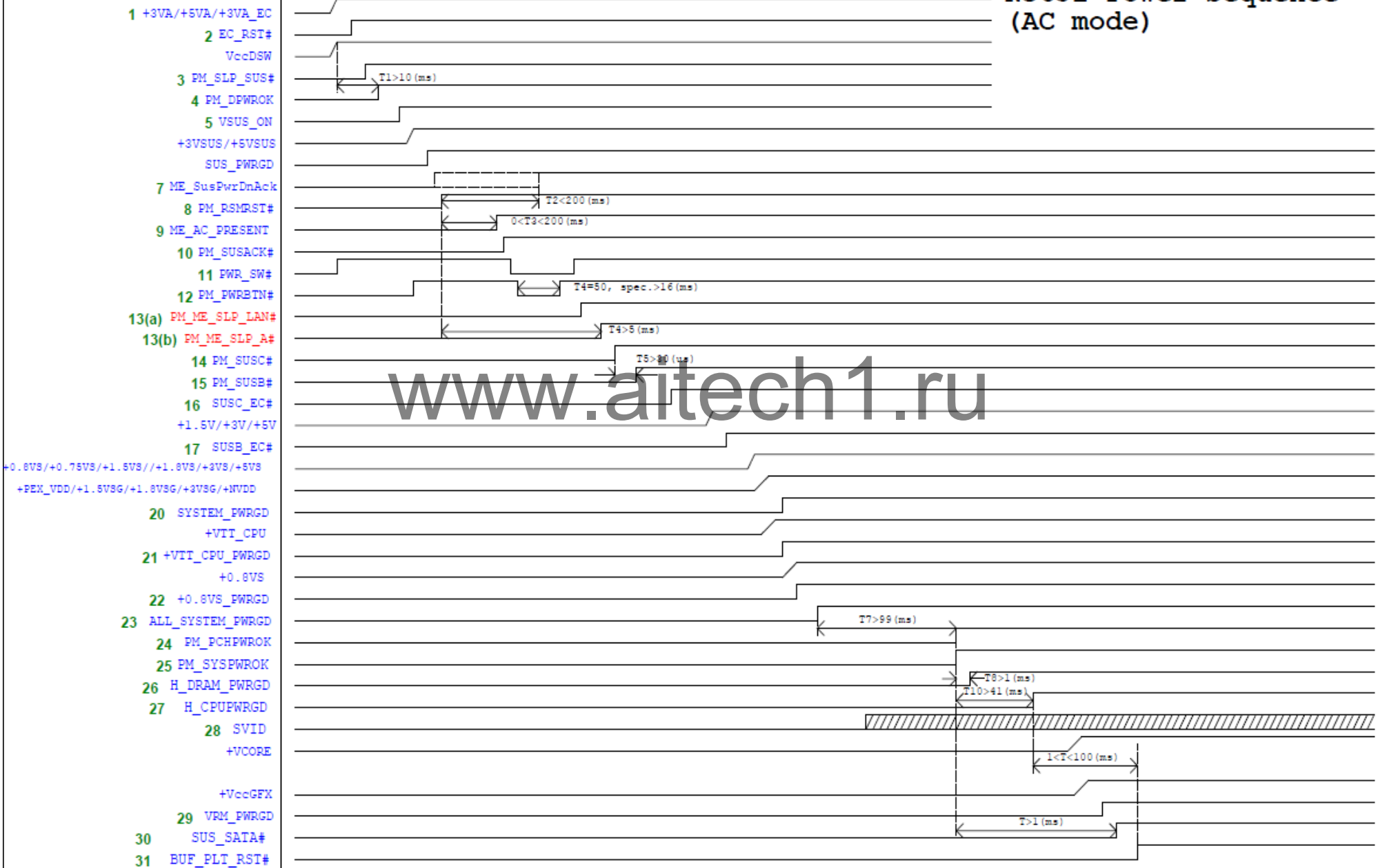
POWER ON SEQUENCE



POWER ON SEQUENCE (AC Mode)

AC-IN Mode

N56JZ Power Sequence (AC mode)



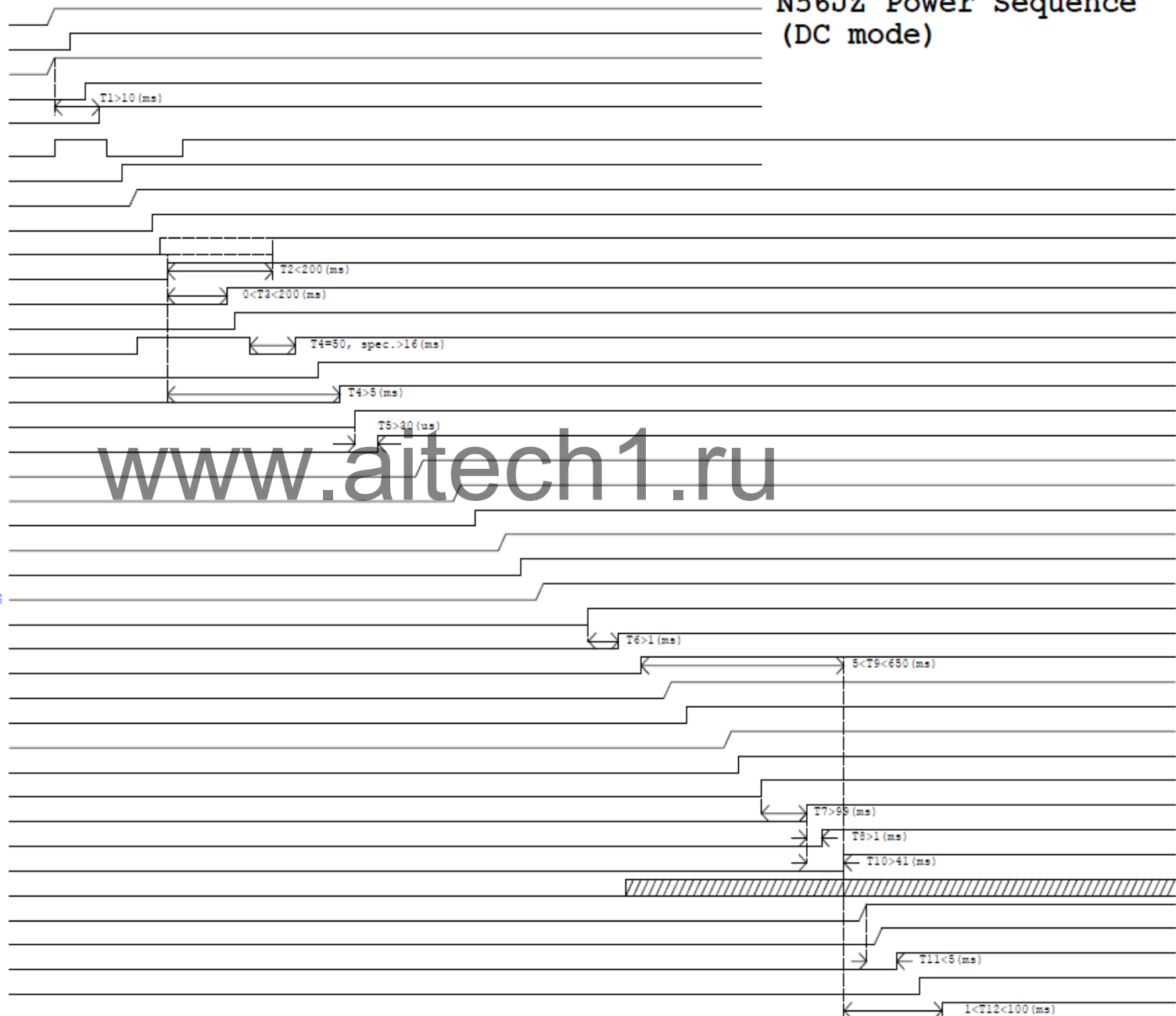
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POWER ON SEQUENCE (DC Mode)

DC-IN Mode

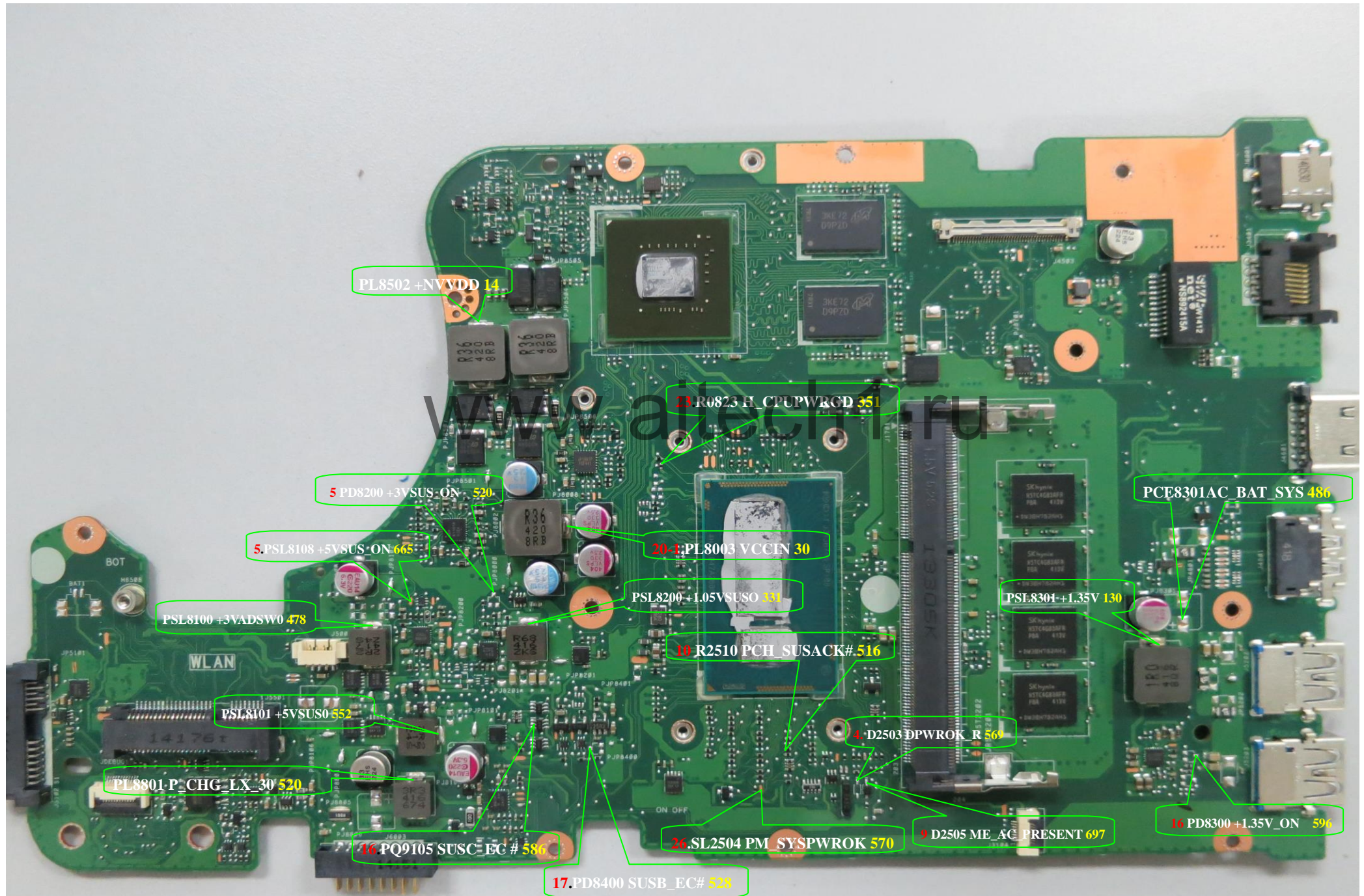
N56JZ Power Sequence
(DC mode)

- 1 +3VA/+5VA/+3VA_EC
- 2 EC_RST#
VccDSW
- 3 PM_SLP_SUS#
- 4 PM_DFWROK
- 5 PWR_SW#
- 6 VSUS_ON
+3VSUS/+5VSUS
- 7 SUS_PWRGD
- 8 ME_SusPwrDnAck
- 9 PM_RSMRST#
- 10 ME_AC_PRESENT
- 11 PM_SUSACK#
- 12 PM_PWRBTN#
- 13(a) PM_ME_SLP_LAN#
- 13(b) PM_ME_SLP_A#
- 14 PM_SUSC#
- 15 PM_SUSB#
+1.05VM_LAN
+1.05VM/+3VM
- 16 SUSC_EC#
+1.5V/+3V/+5V
- 17 SUSB_EC#
+0.75VS/+1.5VS//+1.8VS/+3VS/+5VS
- 18 ME_+VM_PWRGD
- 19 ME_PWROK
- 20 SYSTEM_PWRGD
+VTT_CPU
- 21 +VTT_CPU_PWRGD
+0.8VS
- 22 +0.8VS_PWRGD
- 23 ALL_SYSTEM_PWRGD
- 24 PM_PCHPWROK
- 25 H_DRAM_PWRGD
- 26 H_CPUPWRGD
- 27 SVID
+VCORE
+VccGFX
- 28 VRM_PWRGD
- 29 PM_SYSPWROK
- 30 BUF_PLT_RST#



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Signal Measure Point-Bottom



Signal Measure Point-Top

